

The Way Ahead in Software Engineering

...or, replacing artists with disciplined grownups.

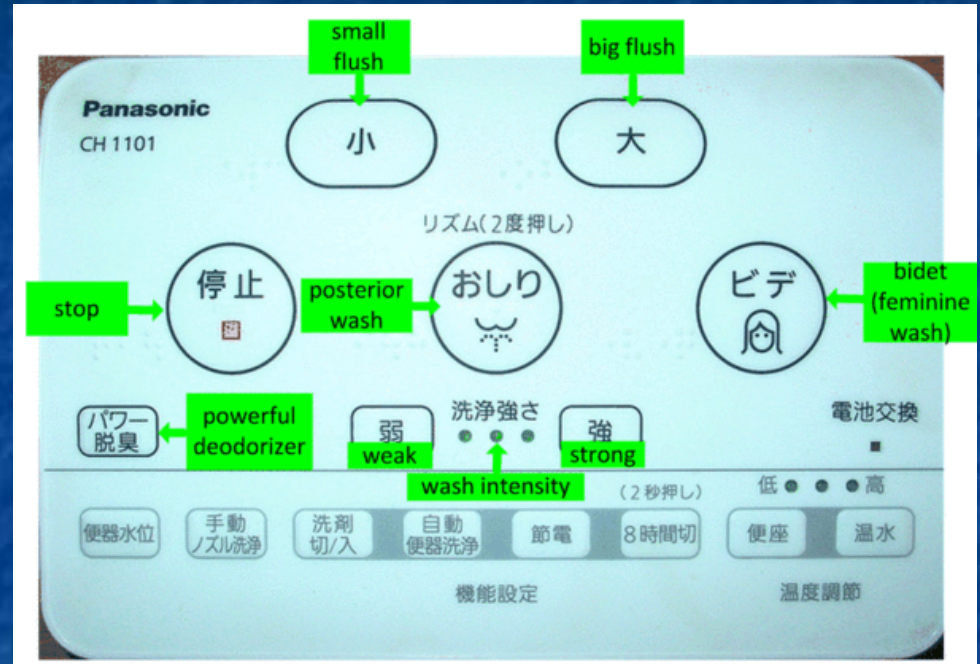
Jack Ganssle

Embedded Systems



Other Embedded Systems





“When the software really has to work,
use Ada.”

State of the Art

The current state of the art in embedded firmware: is it ED-12C? 61508? 50128?

Is it dominated by Ada? SPARK?

What about Correctness by Construction?

How to Start a Holy War

The *only* correct way to position braces:

```
void function(){  
}
```


How to Start a Holy War

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```
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{  
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```

How to Start a Holy War

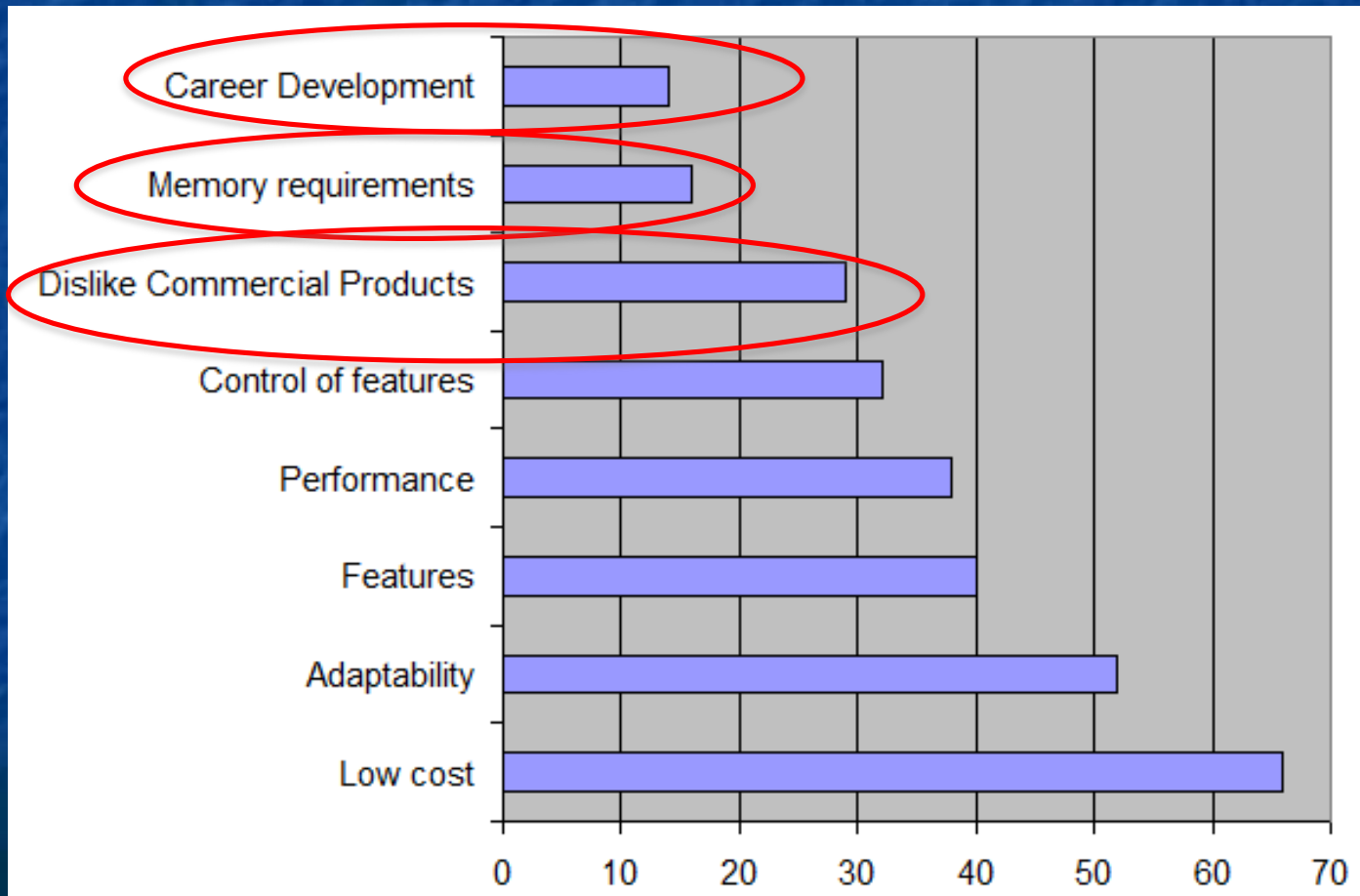
The *only* correct way to position braces:

```
void function()  
{  
}
```


How to Start a Holy War

Or, knock Linux

Why Did You Select Linux?



Linux: 161k functions
average complexity=4.94

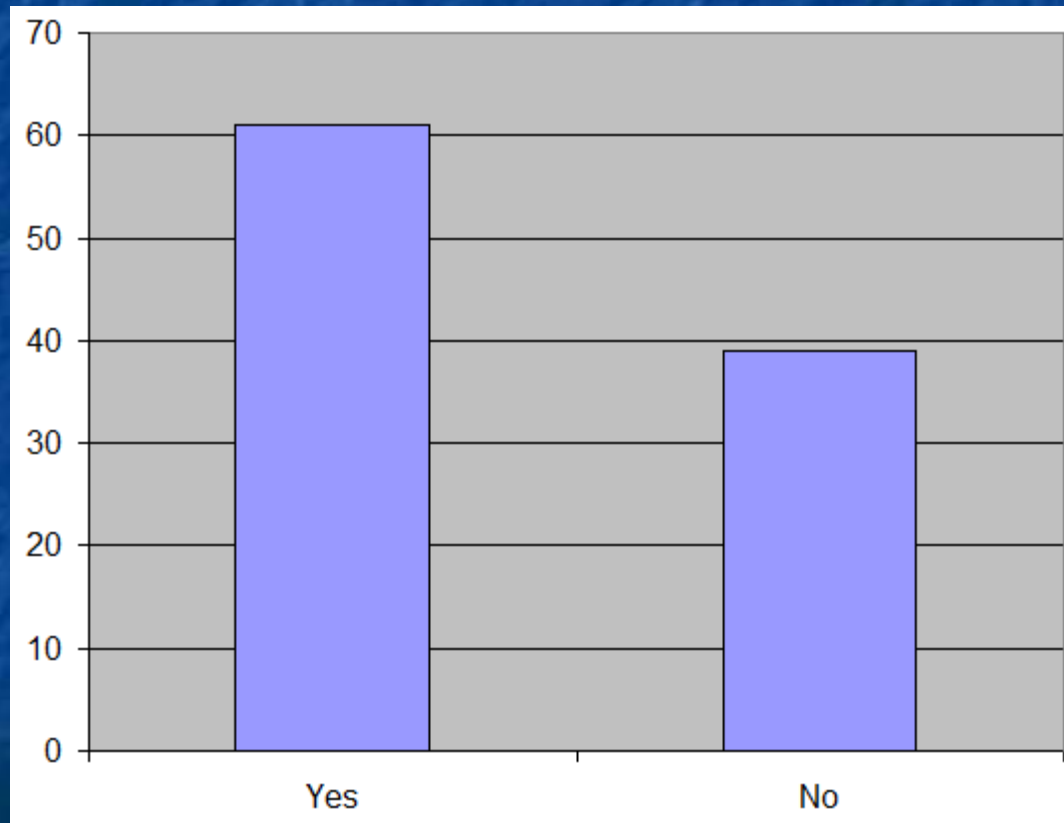
750 functions: over 50
150 functions : over 100!

2261 LOC, complexity=352
86 comment lines

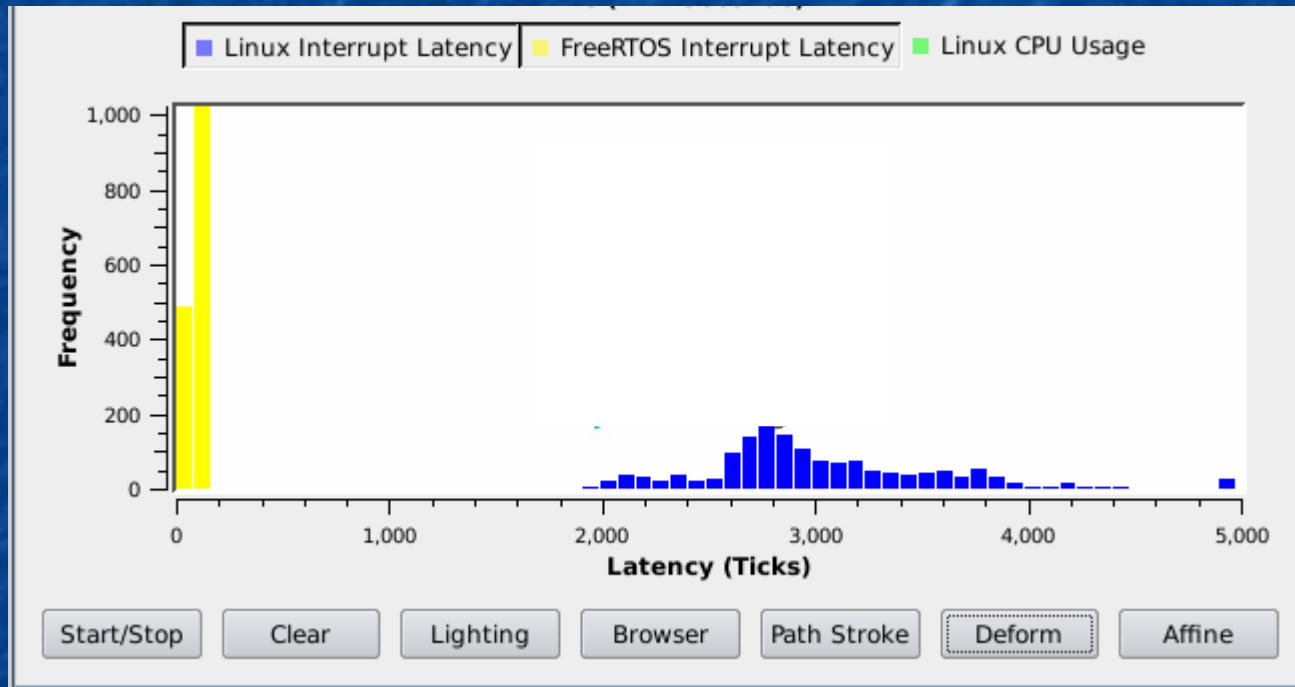
```
2  /*
3   *   ioctl routine
4   */
5
6   static int
7   zoran_do_ioctl (struct inode *inode,
8                   struct file *file,
9                   unsigned int cmd,
10                  void *arg)
11 {
12     struct zoran_fh *fh = file->private_data;
13     struct zoran *zr = fh->zr;
14     /* CAREFUL: used in multiple places here */
15     struct zoran_jpg_settings settings;
16
17     /* we might have older buffers lying around... We don't want
18      * to wait, but we do want to try cleaning them up ASAP. So
19      * we try to obtain the lock and free them. If that fails, we
20      * don't do anything and wait for the next turn. In the end,
21      * zoran_close() or a new allocation will still free them...
22      * This is just a 'the sooner the better' extra 'feature'
23      *
24      * We don't free the buffers right on munmap() because that
25      * causes oopses (kfree() inside munmap() oopses for no
26      * apparent reason - it's also not reproduceable in any way,
27      * but moving the free code outside the munmap() handler fixes
28      * all this... If someone knows why, please explain me (Ronald)
29      */
30     if (mutex_trylock(&zr->resource_lock)) {
31         /* we obtained it! Let's try to free some things */
32         if (fh->jpg_buffers.ready_to_be_freed)
33             jpg_fbbuffer_free(file);
34         if (fh->v4l_buffers.ready_to_be_freed)
35             v4l_fbbuffer_free(file);
36
37         mutex_unlock(&zr->resource_lock);
38     }
39
40     switch (cmd) {
41
42     case VIDIOCGCAP:
43     {
44         struct video_capability *vcap = arg;
45
```


Do You Have Hard Real-Time Requirements?

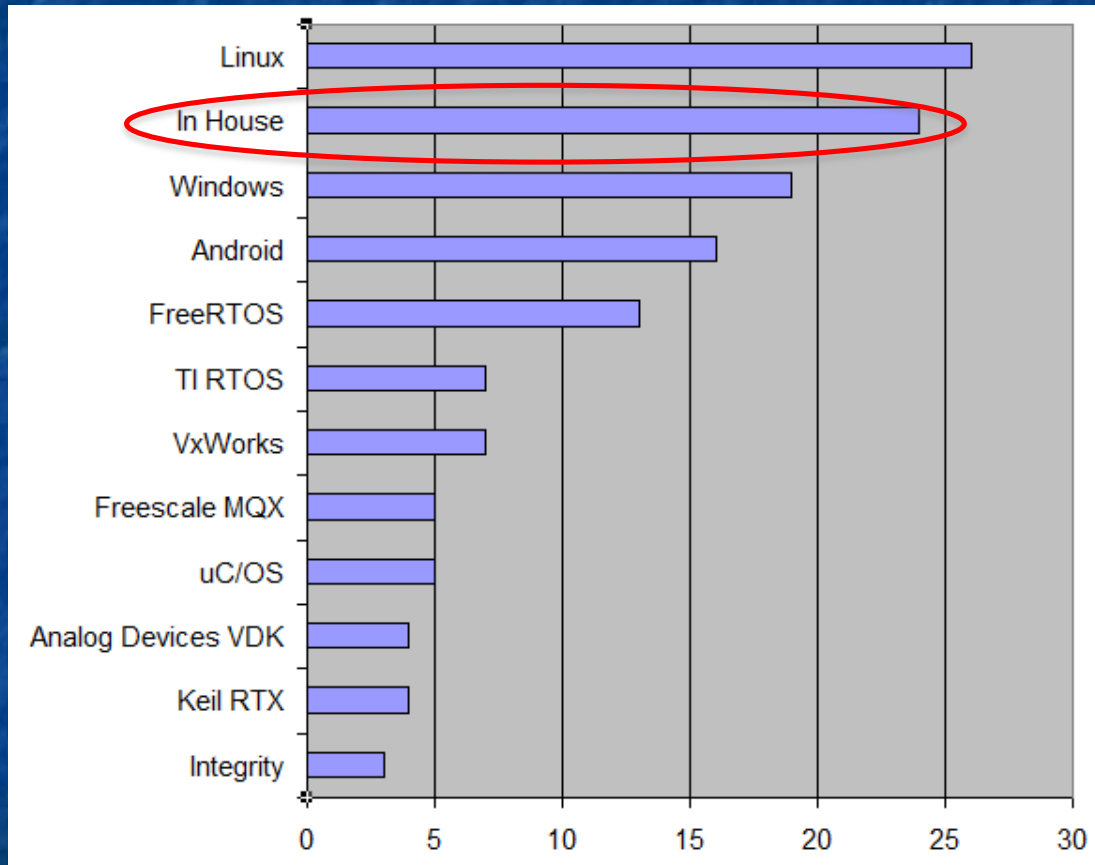
Percent



Linux vs RTOS

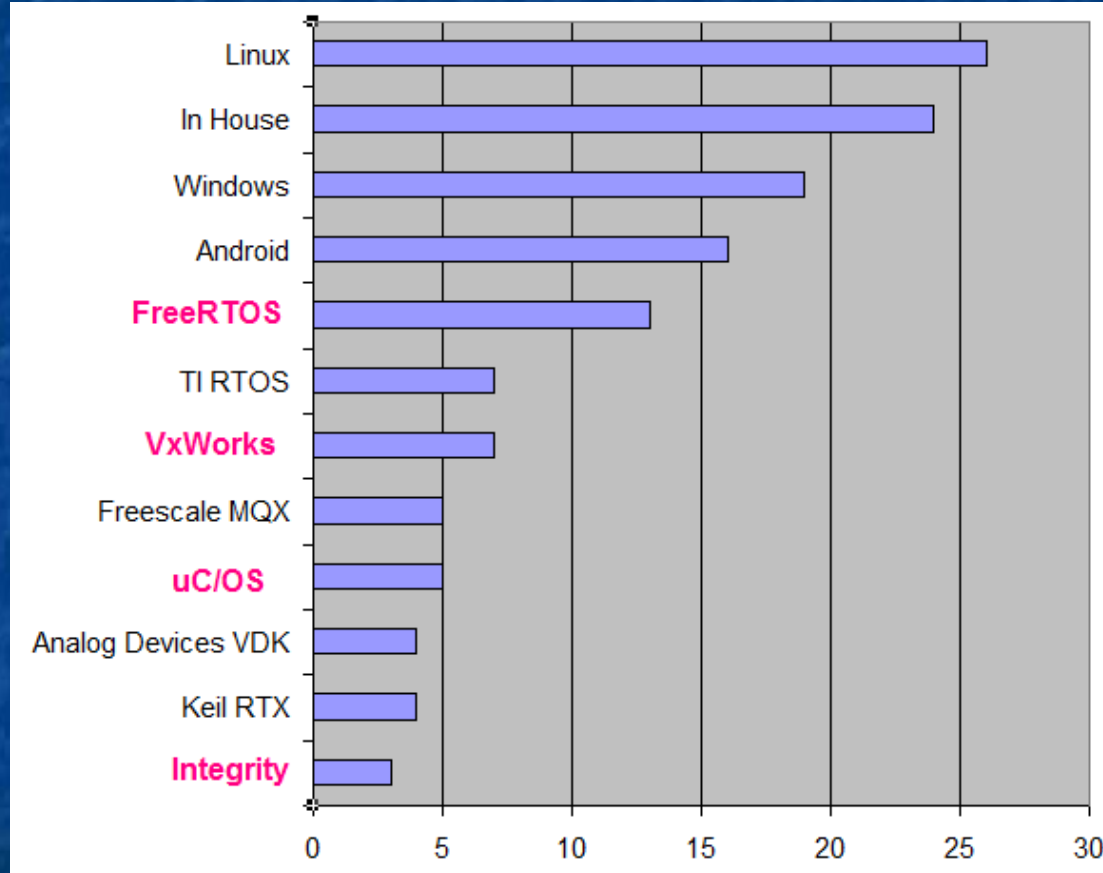


RTOS You're Using



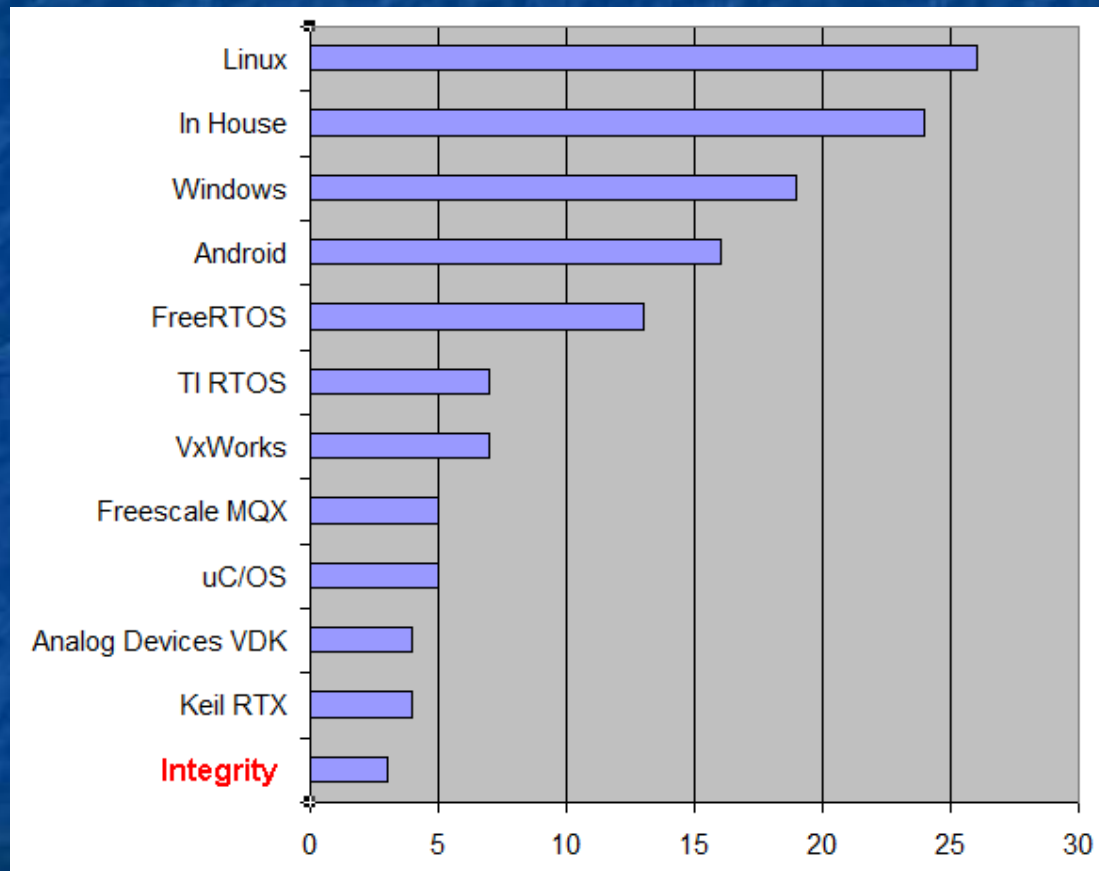
% Using the indicated RTOS

Is it a Proven RTOS?



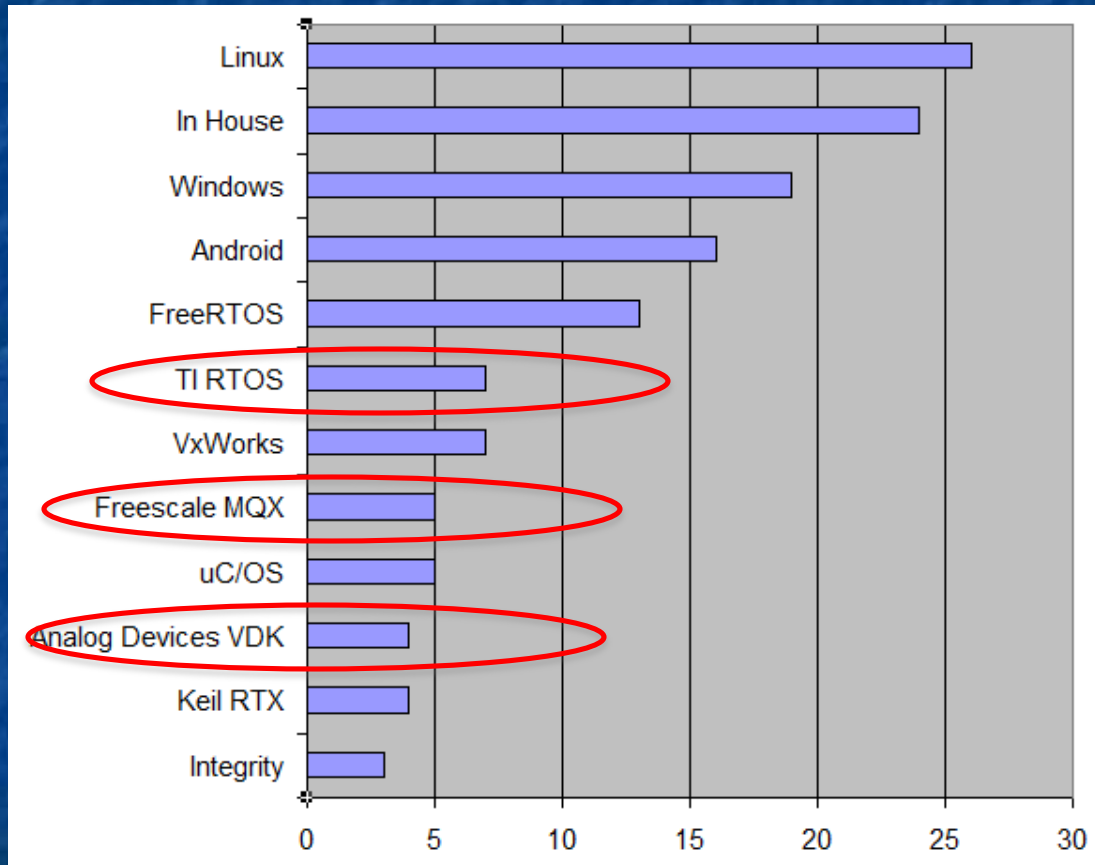
% Using the indicated RTOS

Security? EAL 5 or Higher



% Using the indicated RTOS

Vendor Lock-in?



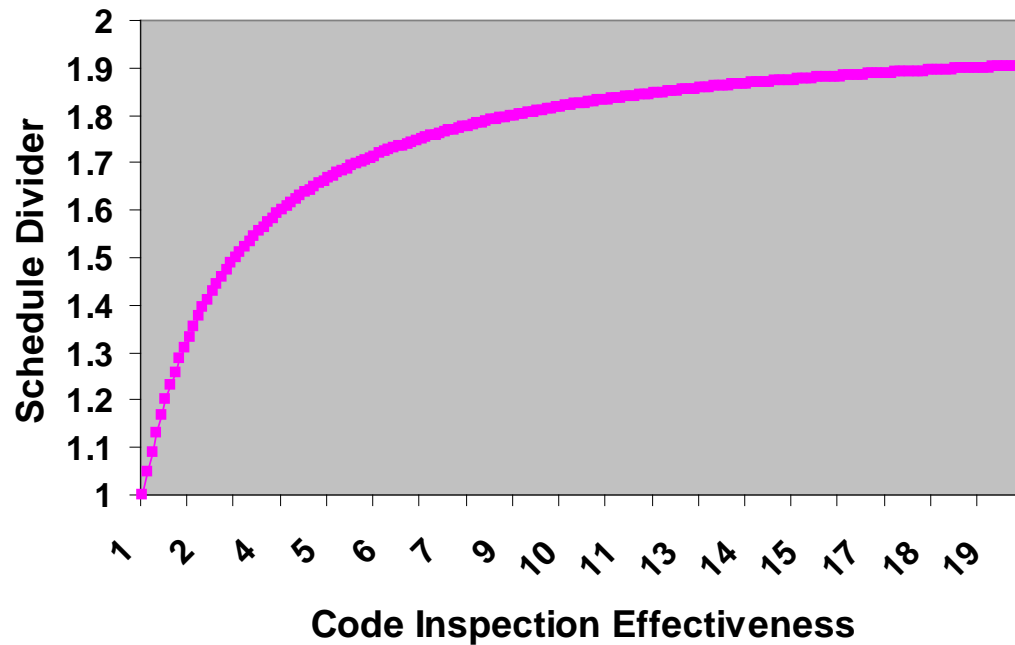
% Using the indicated RTOS

Code Inspections

- HP: 1 defect/4 hrs test, vs. 4.4/hr via inspection
- Russell: inspections 20x faster than testing
- IBM removes 82% of defects *before* testing!
- JPL: inspections 10 to 34x cheaper than test

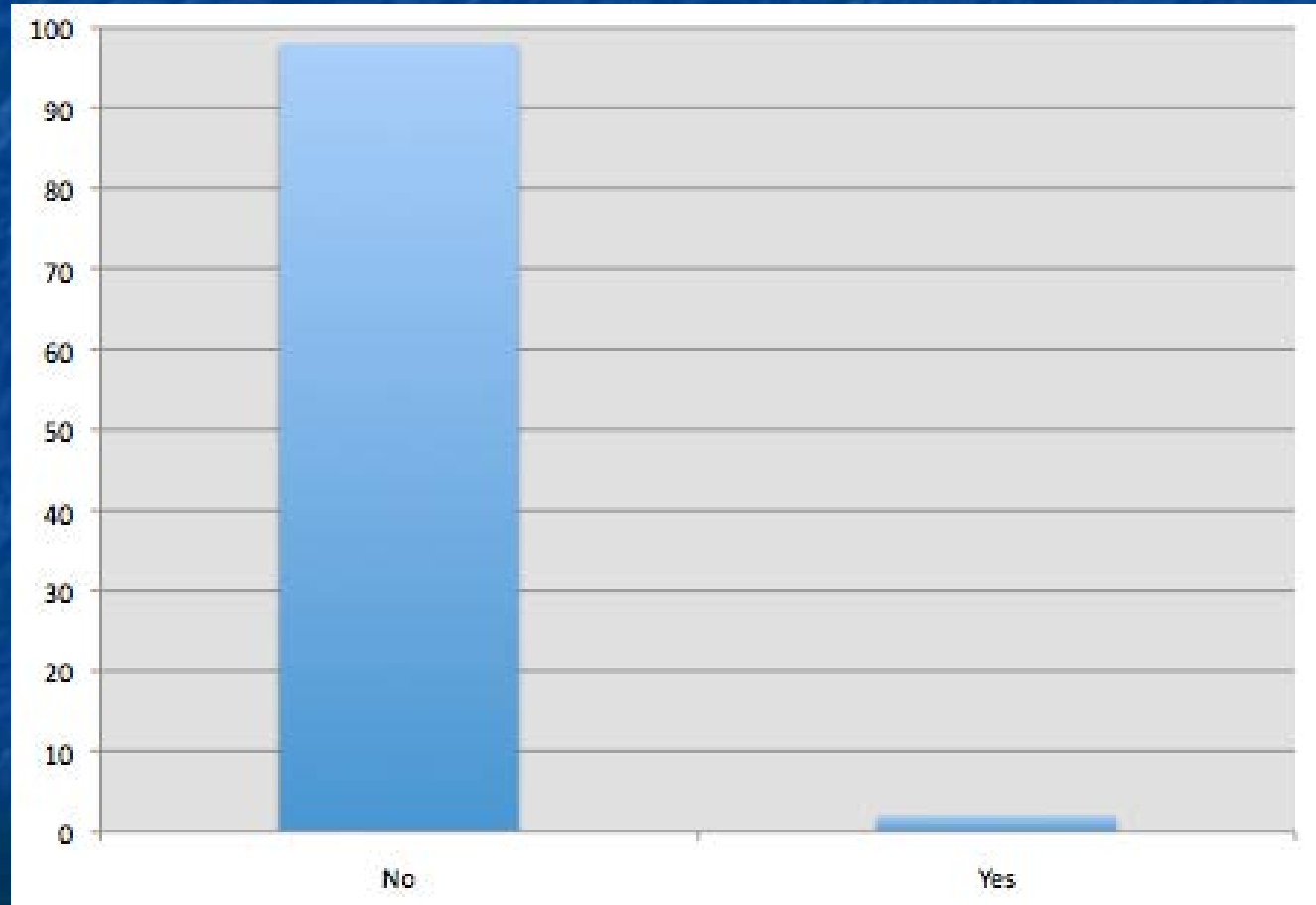
HP: In 22 projects testing only tested 1/2 the code
Glass: Testing exercises 55% of the code

Schedule Vs CI Effectiveness



Do You Routinely Use Inspections?

Percent



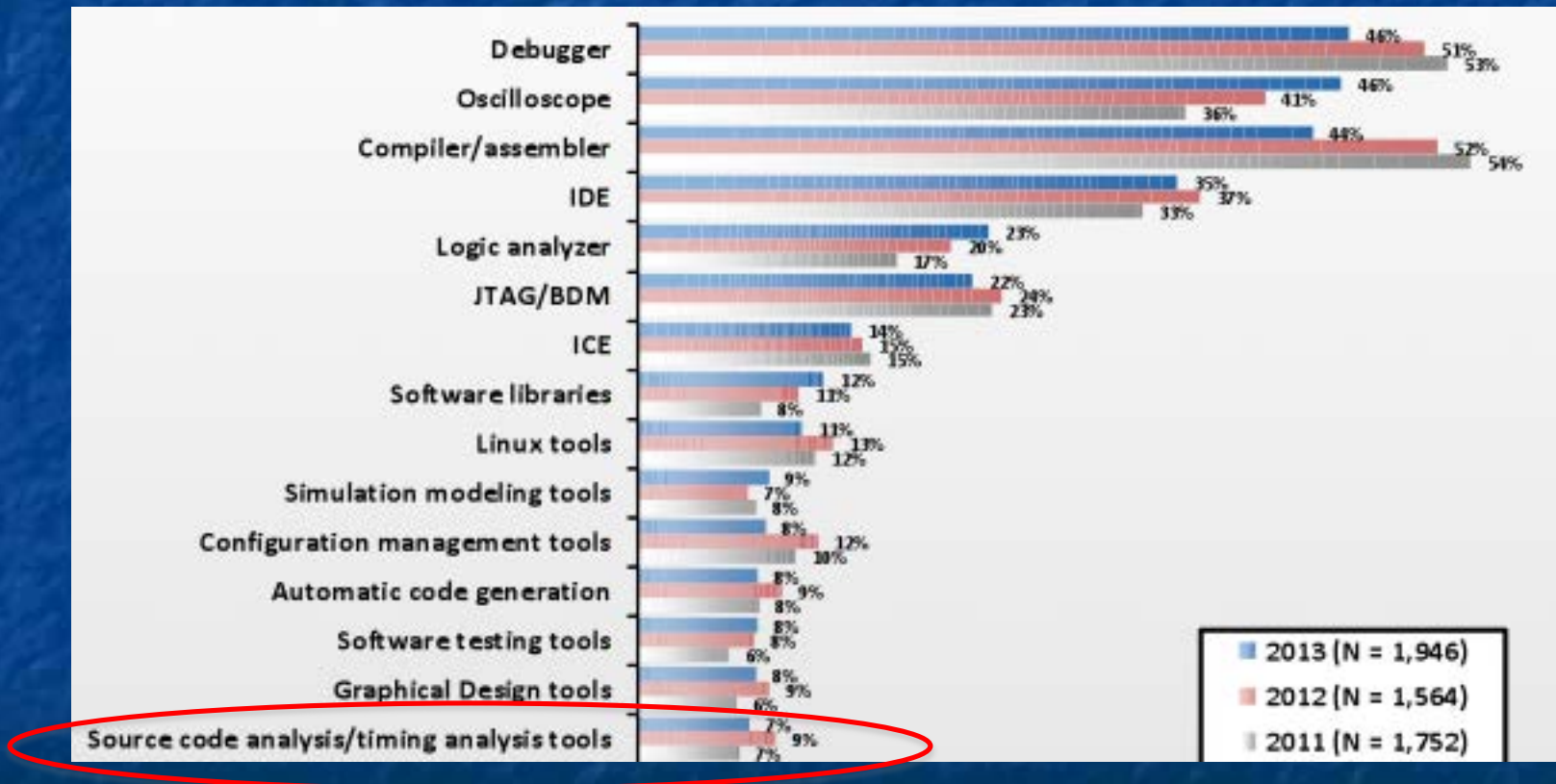
Static Analyzers

- Polyspace
- Klocwork
- Coverity
- Grammmatech
- Green Hills

On one infusion pump with 200KLOC:

Warning Class	Actual Problems
Cast Alters Value	29
Missing Return Statement	1
Null Pointer Dereference	28
Redundant Condition	4
Uninitialized Variable	36
Unreachable Code	20
Useless Assignment	9

Static Analyzers



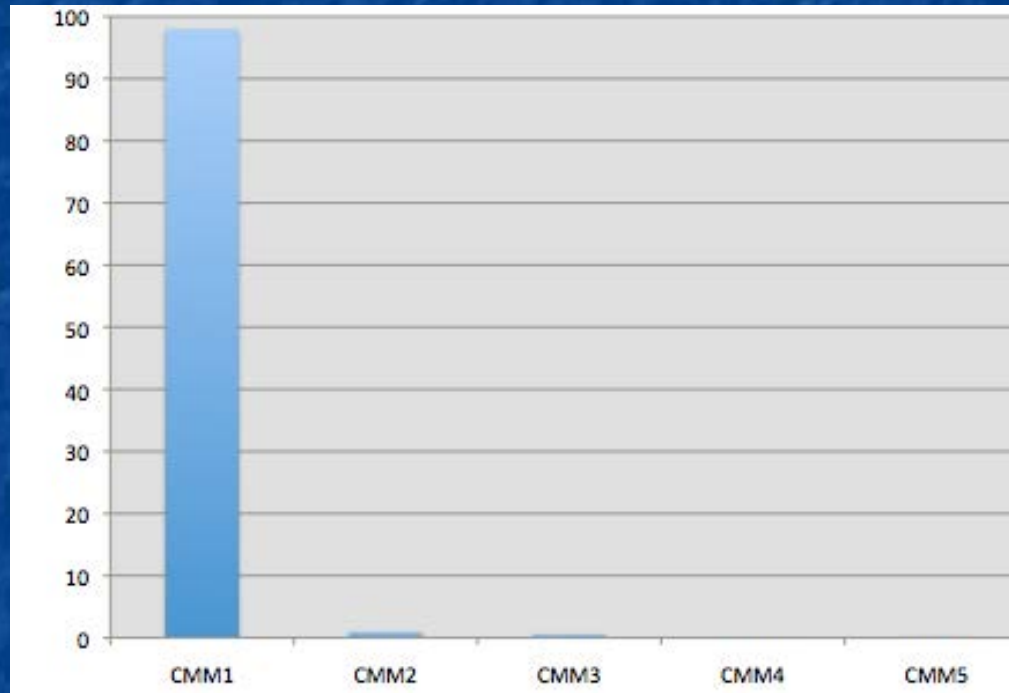
CMM: Typical Shipped Defects

<i>CMM Level</i>	<i>Defect Potential</i>	<i>Removal Efficiency</i>	<i>Delivered Defects</i>
CMM1	50	80%	10
CMM2	40	90%	4
CMM3	30	95%	1.5
CMM4	20	97%	.8
CMM5	10	99%	.1

The Cost to Produce Good Code

	SIL1	SIL2	SIL3	SIL4
CMM1	1.00	1.15	1.28	1.70
CMM2	0.94	1.08	1.20	1.60
CMM3	0.74	0.85	0.95	1.26
CMM4	0.56	0.65	0.72	0.95

Use of CMM



Use of PSP – about 0%

Design in Years Past

- "If you think good architecture is expensive, try bad architecture." - *Brian Foote and Joseph Yoder*
- "Good design adds value faster than it adds cost." - *Thomas Gale*
- "I believe that good design is magical and not to be lightly tinkered with. The difference between a great design and a lousy one is in the meshing of the thousand details that either fit or don't."
- *Ted Nelson*

Design Today

- "Get a few people together and spend a few minutes sketching out the design. Ten minutes is ideal – half an hour should be the most time you spend to do this. After that, the best thing to do is to let the code participate in the design session – move to the machine and start typing in code." - Ron Jeffries
- "The larger the scale, the more you must rely on emergence." - Kent Beck

Language Choices

Keil Software Announces...

COBOLTM for the 8051

A P R I L 1, 2 0 0 5

Attention Managers

Are you having trouble finding software developers for your embedded projects?

Thousands of programmers learned COBOL in order to fix Y2K bugs. Since this crisis has passed, the demand for COBOL programmers in Data Processing is returning to normal. With Keil **COBOL^{for the 8051}** you can tap this underutilized labor pool for your Embedded Systems projects!

Keil **COBOL^{for the 8051}** is based on the ANSI X3.23-1985 standard. Some features that are rarely useful in 8-bit microcontroller applications, such as the sort/merge facility and sophisticated file access methods, are omitted. Keil language extensions meet the unique requirements of embedded systems in a language consistent with standard COBOL.



1501 10th Street, Suite 110
Plano, TX 75074

Toll Free: **800-348-8051**
Phone: 972-312-1107
FAX: 972-312-1159

Attention Engineers

Any standard language requires some adaptation when implemented for the 8051. Keil **COBOL^{for the 8051}** extensions include:

- ☛ Everything the compiler and linker must know about the target system may be specified in the ENVIRONMENT DIVISION: target processor, memory maps, and more. Specifying the target processor in the OBJECT-COMPUTER paragraph automatically declares all its Special Function Registers.
- ☛ Support for SFRs, other I/O ports, and FLASH memory is based on standard COBOL file-handling syntax. Hardware registers are described as RECORDs and bit fields within registers may be declared using PICTURE clauses, so you can access these resources in a familiar and obvious way with READ and WRITE statements.
- ☛ Your dreams of writing interrupt handlers in COBOL have finally come true!

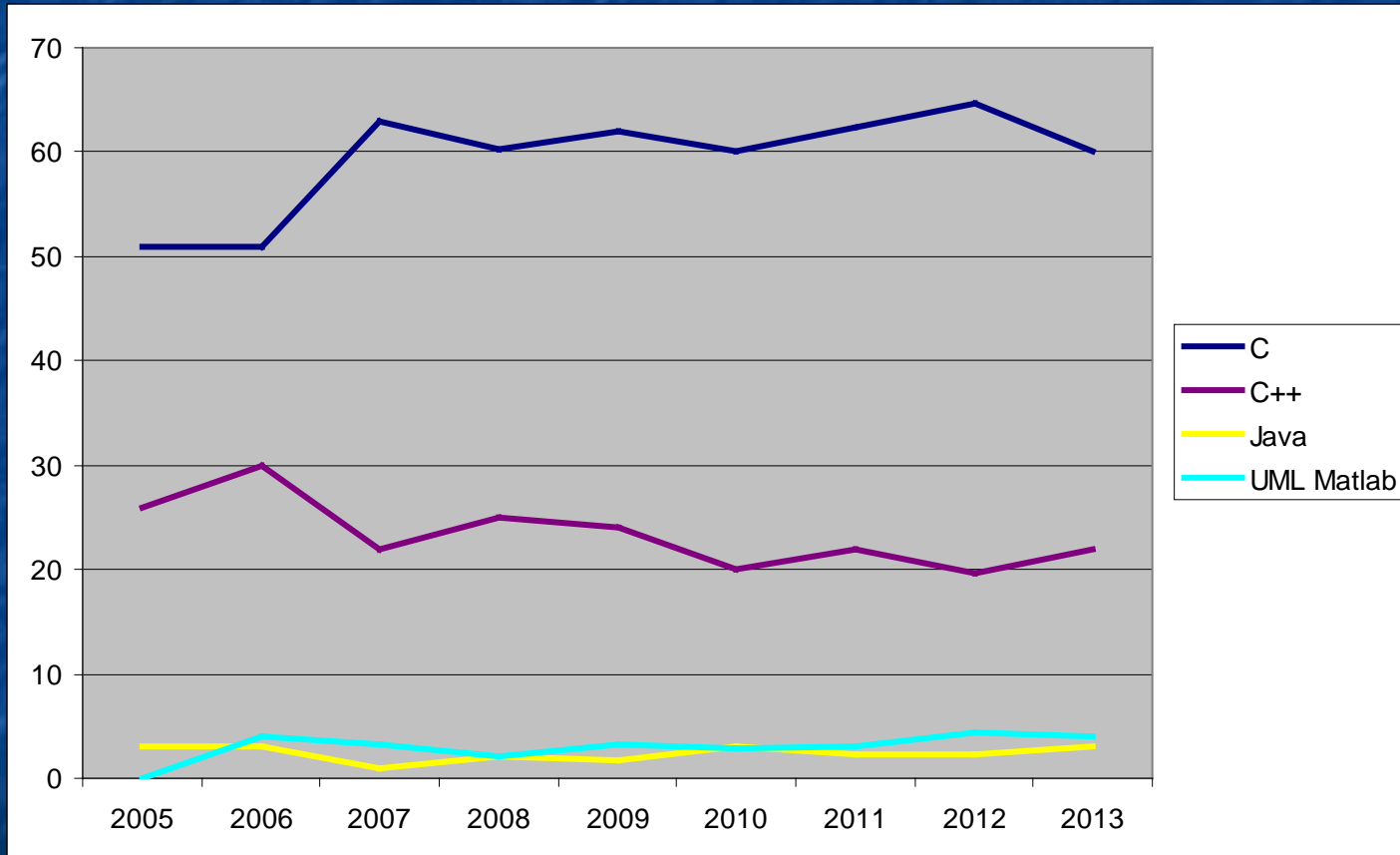
If you're not convinced Keil **COBOL^{for the 8051}** is for you, we also have a highly-regarded C compiler. See...

www.keil.com

Bug Rates

C/C++ (typical)	50 - 100 bugs/KLOC
Ada (typical)	5 - 10
SPARK	pretty much none

Primary Language Used



C Wins

```

        O p
        D,A=6,Z
        O,n=0,W=400
        =( 33,99, 165,
        XGCValues G={ 6,0
        T[]={ 0,300,-20,0,4
        4,-20,4,20,4,-5,4,5,4,
        0,-4,4,-4,-4,4,-4,4,4} ;
        M(T,a[x],H,12); } Ne(C 1,O
        l.t=16; l.e=0; U; } nL(O t,O
        l.d=0; l.f=s; l.t=t; y=-l.c=b;
        %2*x; t=(y|1)%2*y; l.u=(a=s>t?s:
        U; } di(C I){ O p,q,r,s,i=222;C 1;
        -l.s>>9; q=I.c-l.c>>9; r=l.t==8?l.b:
        26) F S+=10; s=(20<<9)/(s|1); B+=p*s;
        R i--&&(x<a[i]-d||x>a[i]+d)); F i; }
        Y){ r++;c=l.f; Y==3){c=l.u; l.t=0;
        (l.s>>9)-++l.a,h-l.a,l.a*2,l.a*2,O
        (b,l.s>>9,h,6); else XDrawPoint(d
        (1,20); K; } Y&&l.t<3&&(di(1)||h>
        A]; }Ne(1,30); Y==1){ E;K; } else
        dL(){ O
        E; }R c--){--
        ,90<<8); if(!l.u){
        ,w,g,(l.s+=l.a)>>9,
        H){ if(h>H&&(c=hi(
        c=l.t=0; } Y==1&&h<H
        N(W<<9),H<<9,1,i+
        1); I[i].d++;
        )R N(3)

        ); K;
        l.u=c; c=0; } Y
        ==2){ l.s+=l.a+B;
        l.a= (l.e-l.s)/(H+
        20-h)|1; l.c+=l.b+D;
        M(b,l.s>>9,l.c>>9,6); }
        } L[i]=1; } } F r; } J(){
        R A) { XFlush(d); v&&sleep(
        3); Z=++v*10; p=50-v; v%2&&hi
        ((a[A]=N(W-50)+25),50)<0 &&A++;
        XClearWindow (d,w); for(B=0; B<A;
        dC(B++)); R Z|dL(){ Z&&!N(p)&&(Z--
        ,nL(1+!N(p),N(W<<9), O,N(W<<9),H<<9,1
        ,0)); usleep(p*200); XCheckMaskEvent(d,
        4,&e)&&&&--S&&nL(4,a[N(A)]<<9,H-10<<9,e.
        xbutton.x<<9,e.xbutton.y<<9,5,0);}S+=A*100;
        B=sprintf(m,Q,v,S); XDrawString(d,w
        ,g,W/3,H/2,m,B); } }

        ,B,
        ,S=0,v=
        ,H=300,a[7]
        231,297,363} ;
        ,~OL,0,1) ; short
        ,-20,4,10,4,-5,4,5,
        -10,4,20),b[]={ 0,0,4,
        C L[222],I[222];dC(O x){
        s){ l.f=l.a=1; l.b=l.u=s;
        a,O b,O x,O y,O s,O p){ C 1;
        l.e=t==2?x:p; x=-l.s=a;s=(x|1)
        t)>>9;l.a=(x<<9)/a;l.b=(y<<9)/a;
        B=D=0; R i--){ l=L[i]; Y>7){ p=I.s
        l.a; s=p*p+q*q; if(s<r*r||I.t==2&&s<
        D+=q*s; } } F O; } hi(O x,O d){ O i=A;
        c,r=0, i=222,h; C 1; R i--){ l=L[i];
        l.u;h=l.c>>9; Y>7){XDrawArc(d,w,g,
        I[i].t--8; l=I[i]; } } else Y==2)M
        h={l.c+=l.b)>>9; Y==4&&l.u){ Ne
        l.s>>9,25)}>0){ dC(c); a[c]=a[--
        -75&&!N(p*77)}{ do{ nL(1,l.s,l.c,

```

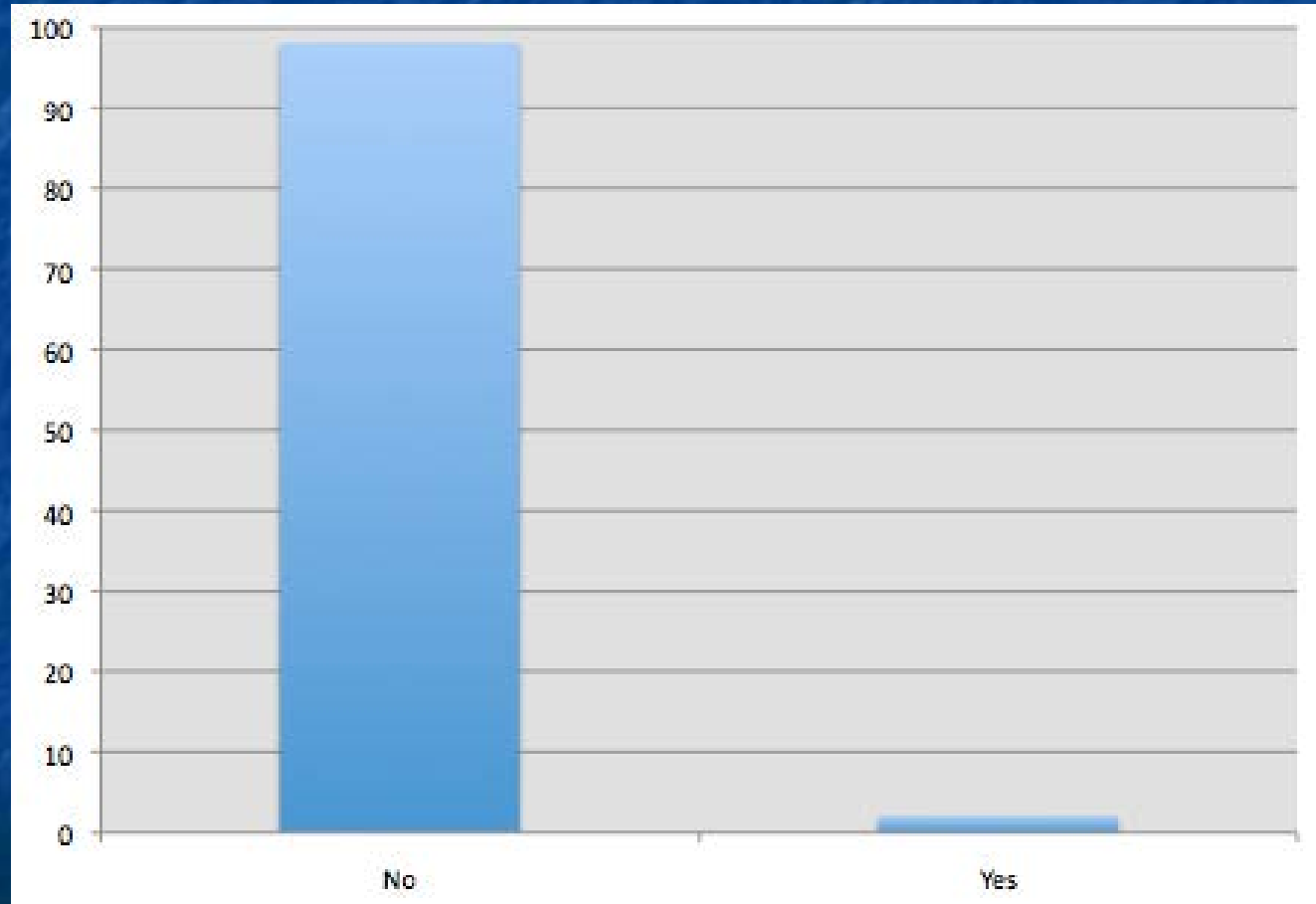
Incomprehensible C

```
*****variable = 0;
```

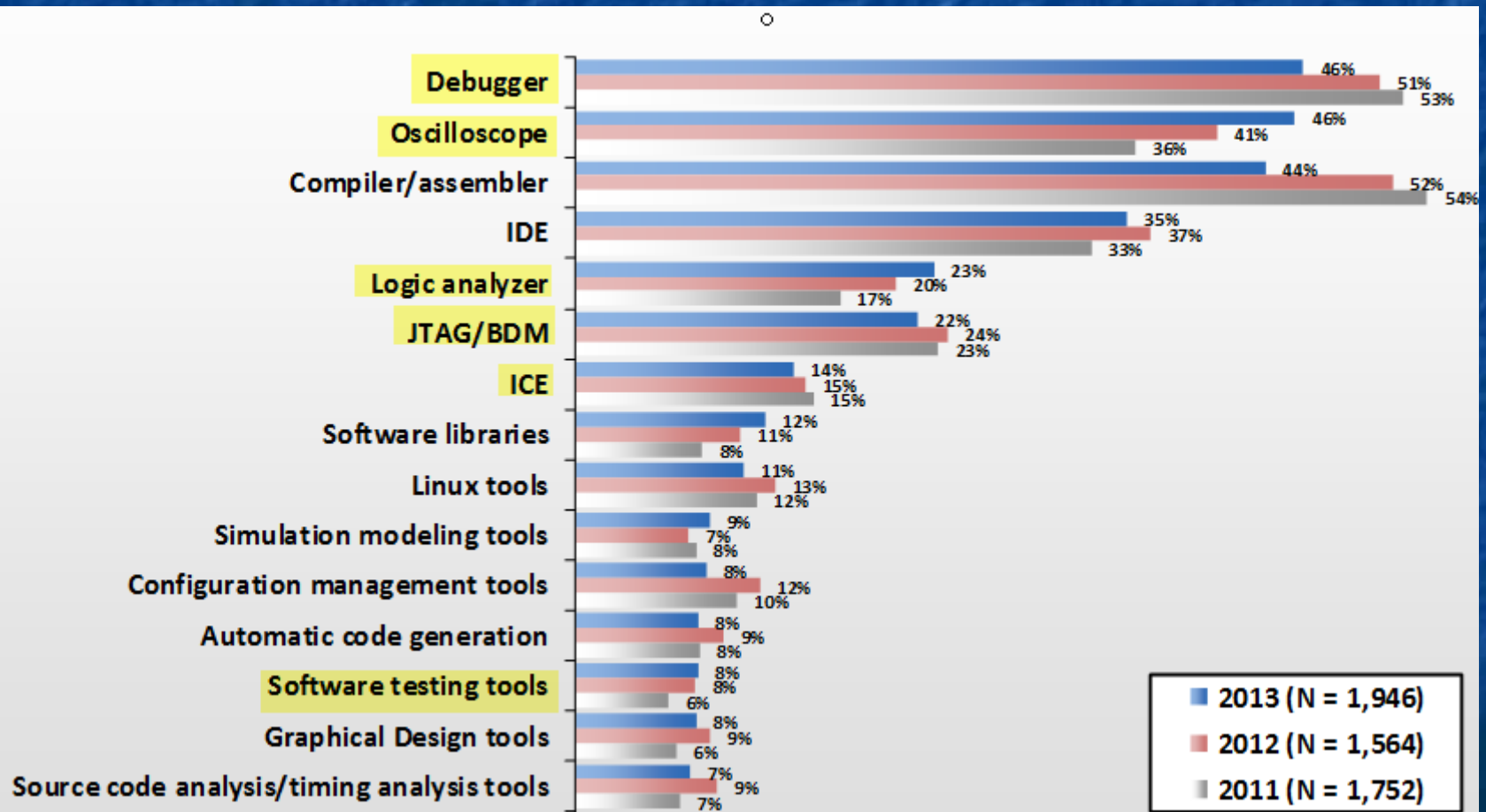
“int” means... what?

Do You Use a Standard?

Percent



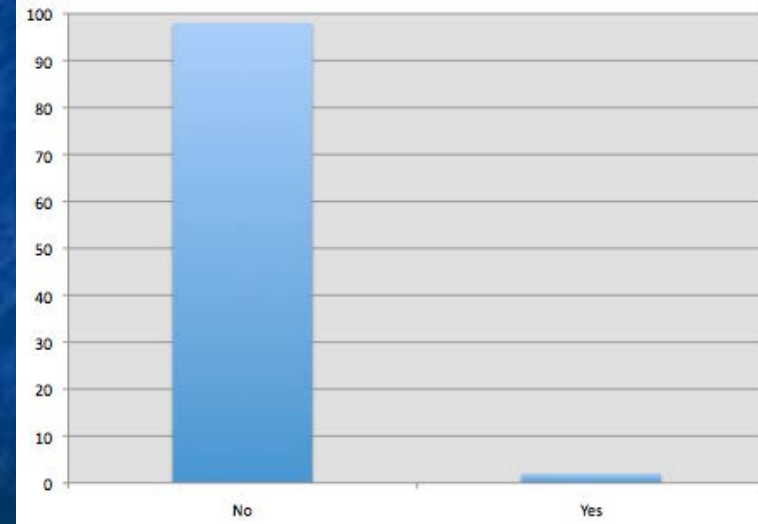
Most Important Tools



Biggest cause of slipped schedules:

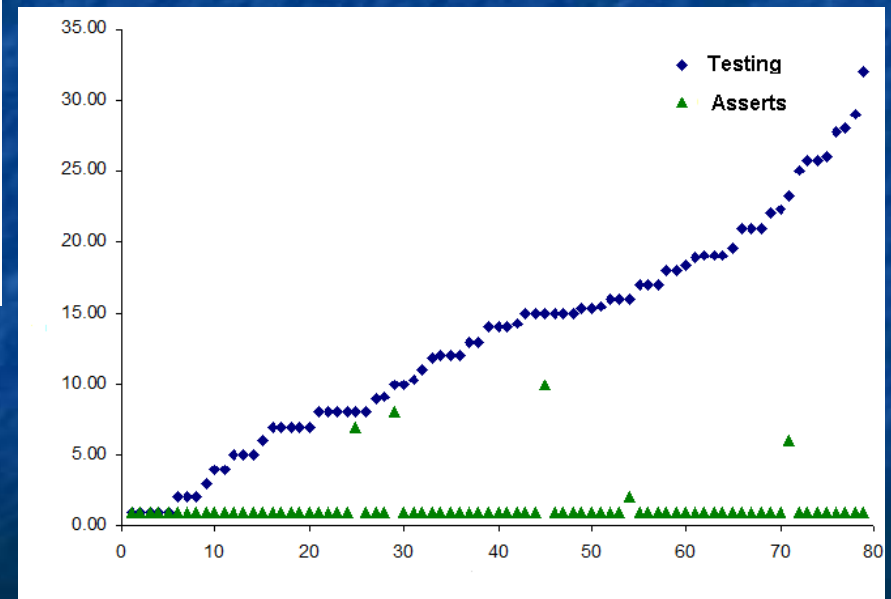
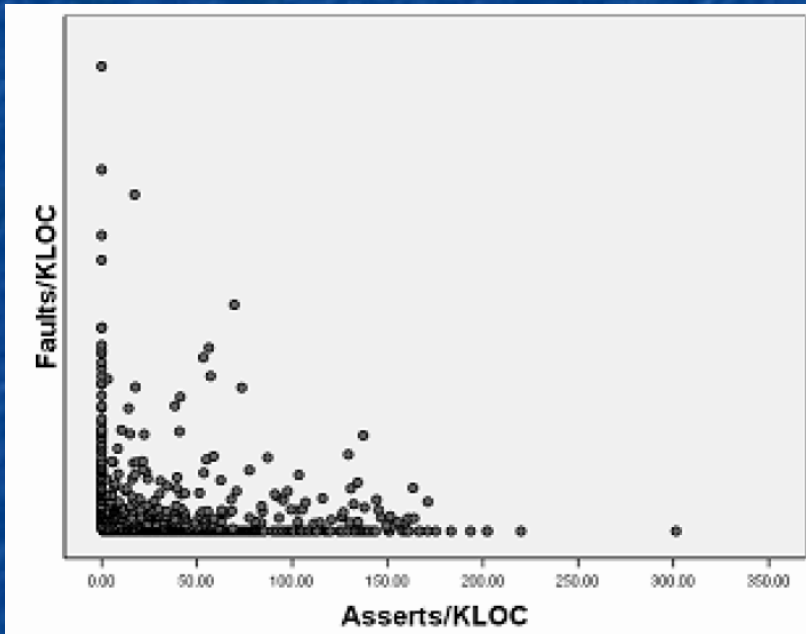
Bugs!

C/C++	50 - 100 bugs/KLOC
Ada	5 - 10
SPARK	pretty much none



Use of inspections

Assertions vs. Bugs



Metrics?



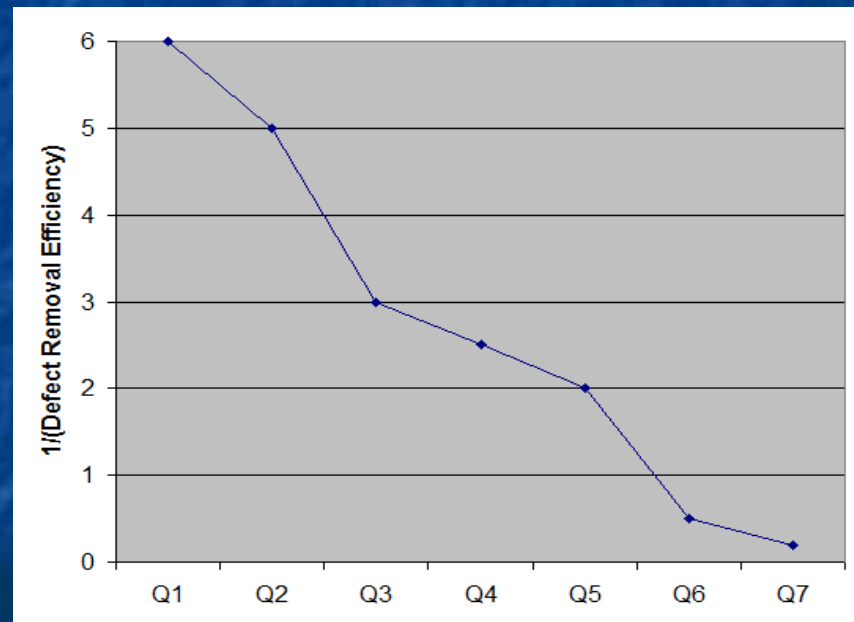
Bug Metrics

<i>Bug source</i>	Malpractice	CMM3
Requirements	15/50%	4/85%
Design	22/50%	6/97%
Coding	25/80%	10/99%
Documents	10/70%	4/98%
Bad fixes	8/50%	1/95%
TOTAL INJECTED	80	25
SHIPPED	50	1

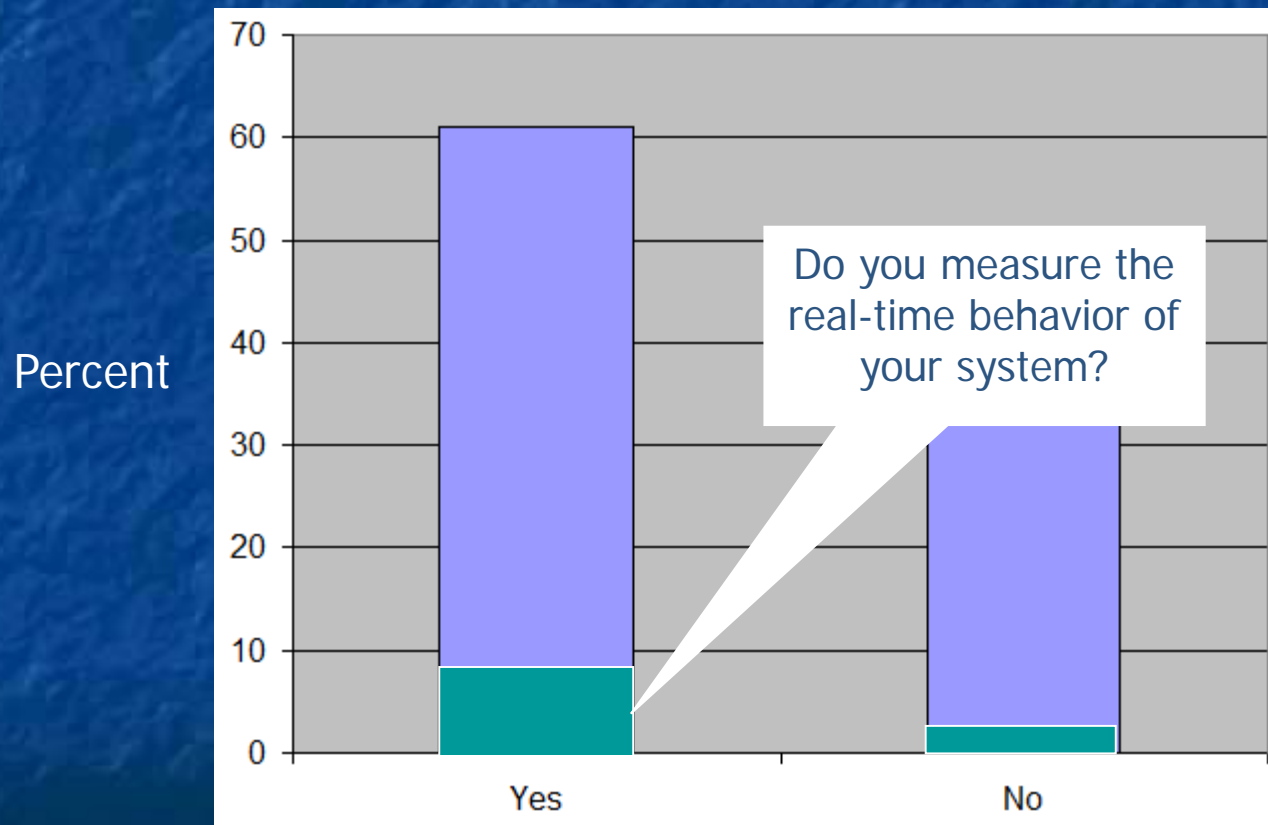
Defect Removal Efficiency

“When these measures were introduced into large corporations such as IBM and ITT, in less than four years the volumes of delivered defects had declined by more than 50%; maintenance costs were reduced by more than 40%; development schedules were shortened by more than 15%. There are no other measurements that can yield such positive benefits in such a short time span.” - Capers Jones

Percent of bugs shipped

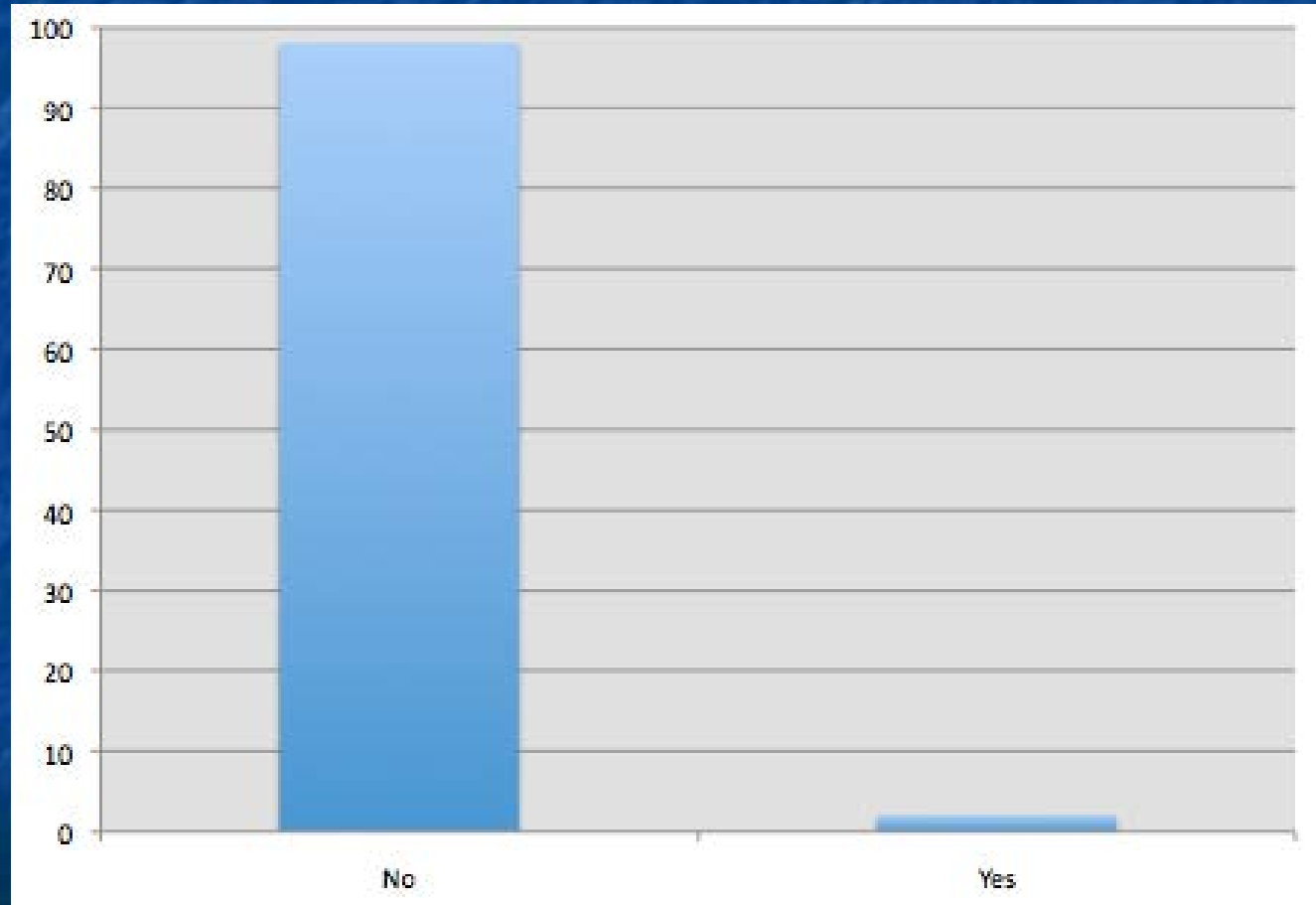


Hard Real-Time Requirements



Do You Measure Anything?

Percent



Are We Professionals?

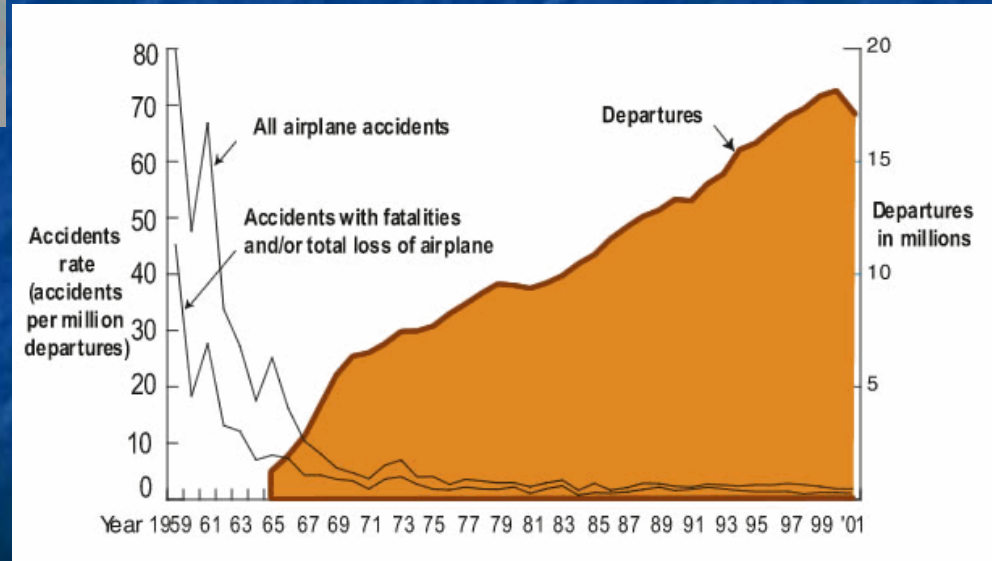


How Does Engineering Become Engineering?



- Montrose Bridge, Scotland 1838
- Menai Strait Bridge, Wales, 1839
- Basse-Chaine Bridge, 1850
- Roche-Bernard Bridge, France
- Wheeling Suspension Bridge, 1854
- Niagara-Lewiston Bridge, 1864
- Niagara-Clifton Bridge, 1889

How Does Engineering Become Engineering?



How Does Engineering Become Engineering?



Iroquois Fire



Triangle Shirtwaist fire



MGM fire

Iroquois Fire Report

"The fire department seemed to be under the impression that they were required only to fight flames and appeared surprised that their department was expected by the public to take every precaution to prevent fire from starting."

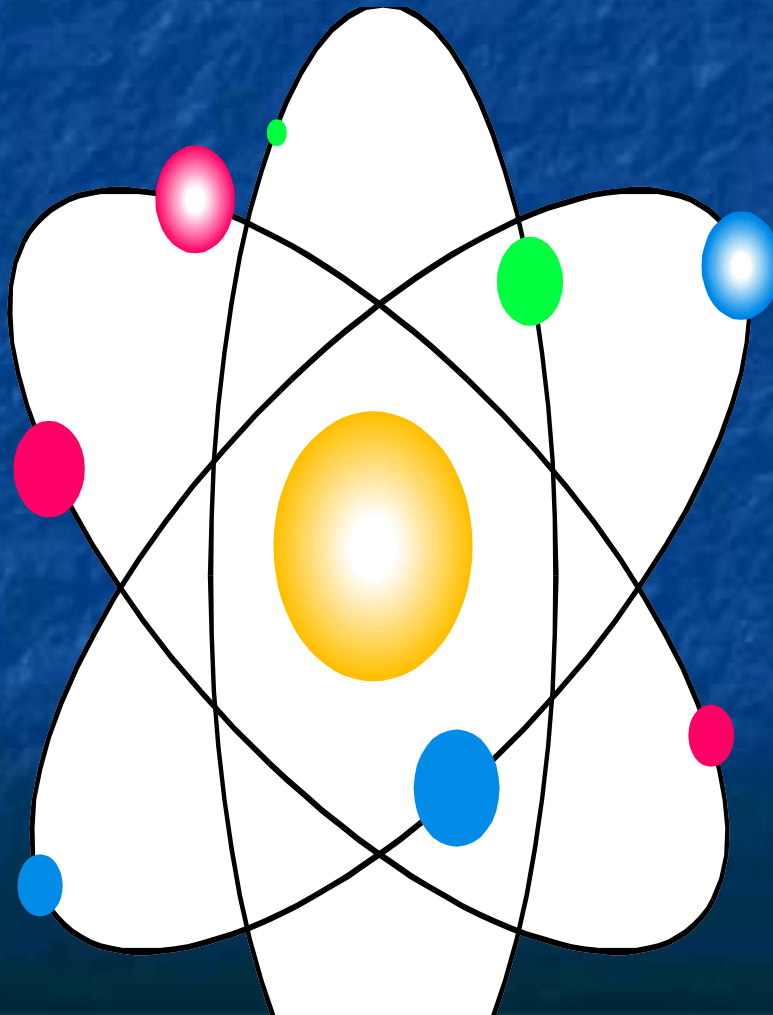
Recalls



Recalls Are Getting Worse

	No. of Pacemakers		No. of ICDs	
	Recalls	Affected Devices	Recalls	Affected Devices
Type of recall or alert				
Class recall				
I	7	5996	2	23 410
II†	18	312 048	12	64 277
III	3	59	1	2358
Safety alert	7	90 397	3	24 600
Total	35	408 500	18	114 645
Type of malfunction‡				
Hardware†	22	204 818	14	75 823
Electrical/circuitry	6	147 248	4	10 141
Battery/capacitor	6	7995	3	30 831
Hermetic seal	5	6447	1	29
Other†	5	43 128	6	34 822
Firmware	8	200 851	2	15 682
Environmental interaction	0	0	2	23 140
Nondevice-related	5	2831	0	0
Total†	35	408 500	18	114 645

Testing Failures



Testing Failures

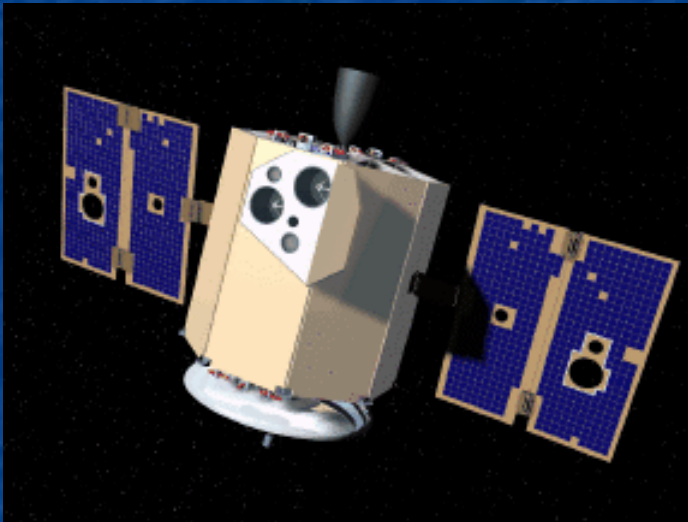


“Although there was limited long duration testing whose purpose was to identify system memory consumption of this type, no problems were detected because the system was not exercised in the same way that it would later be used in flight.”

Testing Failures



We Can't Learn From Disaster



We Can't Learn From Disaster



Uwatec dive
computer



Challenger

Incredibly Sloppy Programming

the bottom of the form. If you need to change any information, click on the Edit link next to the your order.

Shipping Address

Jack Ganssle
PO Box 38346
Baltimore , MD 21231

[Edit](#)

Description	Each	Total
ERLUX/FIBERGLASS BOTTOMKOTE	\$29.99	\$29.99
ACOR/2M ELEMENT- 500 SERIES	\$8.99	\$53.94
	SubTotal	\$83,929,999,999,999.98
	UPS Ground Residential Shipping	\$0.00
	Tax	\$0.00

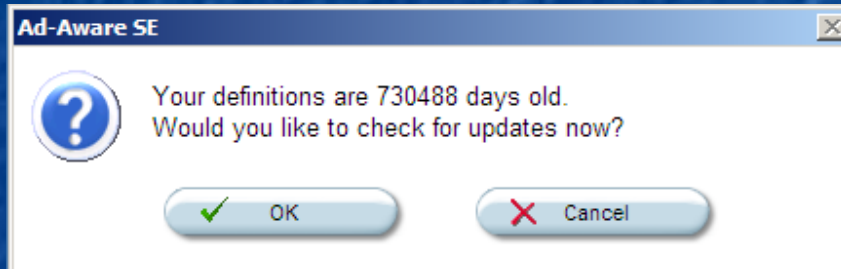
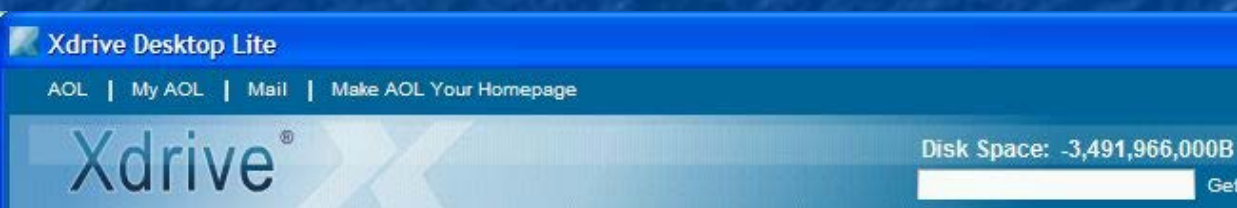
Incredibly Sloppy Programming



Wind Statistic					Wind Report				
Berlin-Tegel (TEGEL) Print Version									
Map: 52.57 13.32 Time zone: UTC +2 Sunrise: 06:03 Sunset: 20:10									
Local date	Wednesday, Apr 18								
Local time	02h	05h	08h	11h	14h	17h	20h	23h	02h
Wind direction									
Wind speed (Knots)	10	8	9999	13	13	12	6	8	7
Cloud cover									
Precipitation (mm/3h)	0	0	-999	0	0	0	0	0	0
Pressure sealevel (hPa)	1011	1012	-999	1013	1013	1013	1014	1015	1016
Air temperature (°C)	8	5	-999	8	9	12	8	7	3

8:29am EDT 26-MAY-2007

Incredibly Sloppy Programming



Cisco Systems Inc CSCO:NASDAQ

Communications Equipment

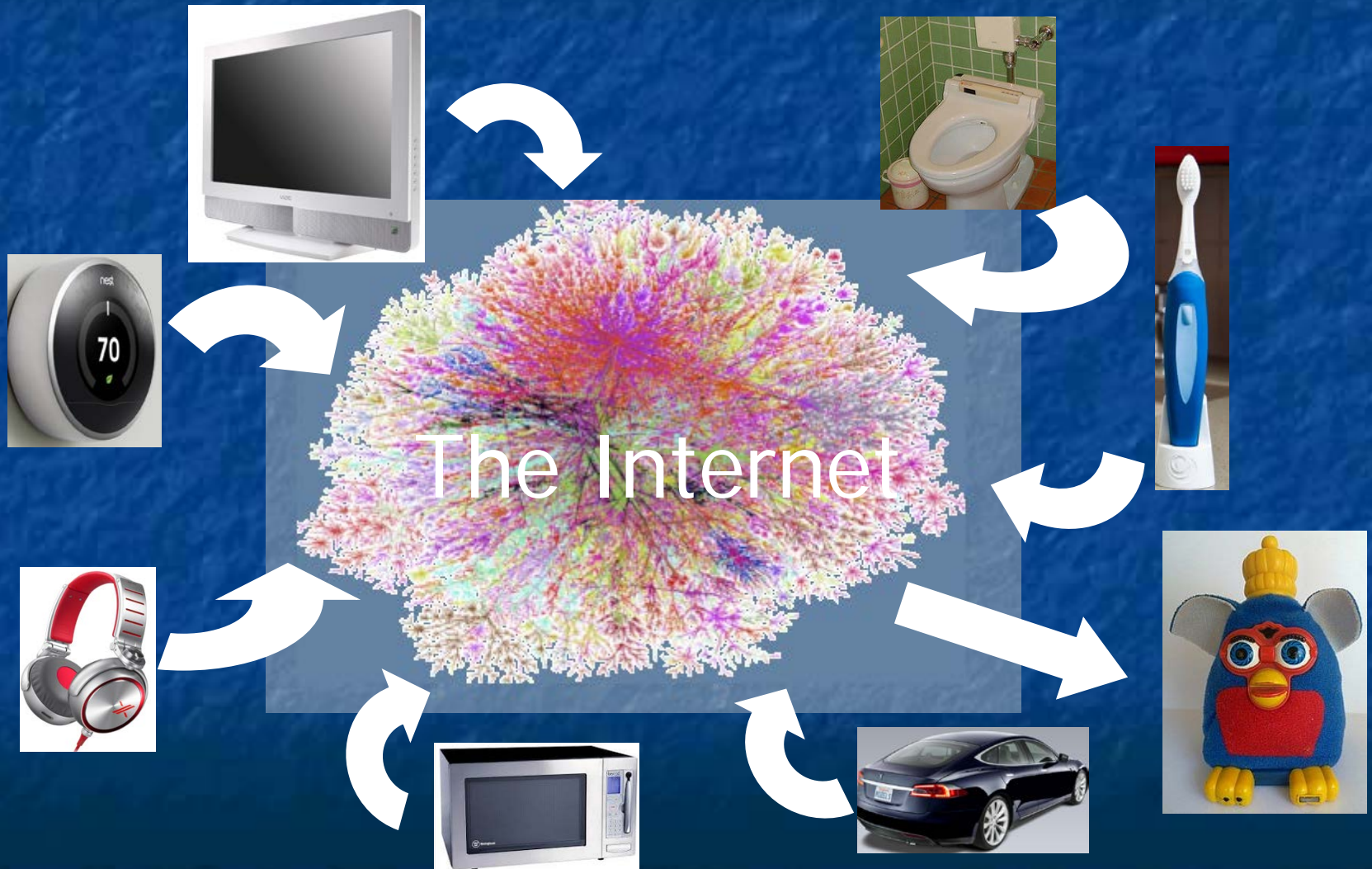
Last

Price	Today's Change	Bid/Size	Ask/Size	Today's Volume
\$20.625	-3,590,848,672.375 (-100.00%)	\$20.54/130	\$20.55/130	9,509,430 Average

As of 10:49 AM ET, 05/08/2013



The Internet of Things



Iroquois Fire Report

"The fire department seemed to be under the impression that they were required only to fight flames and appeared surprised that their department was expected by the public to take every precaution to prevent fire from starting."

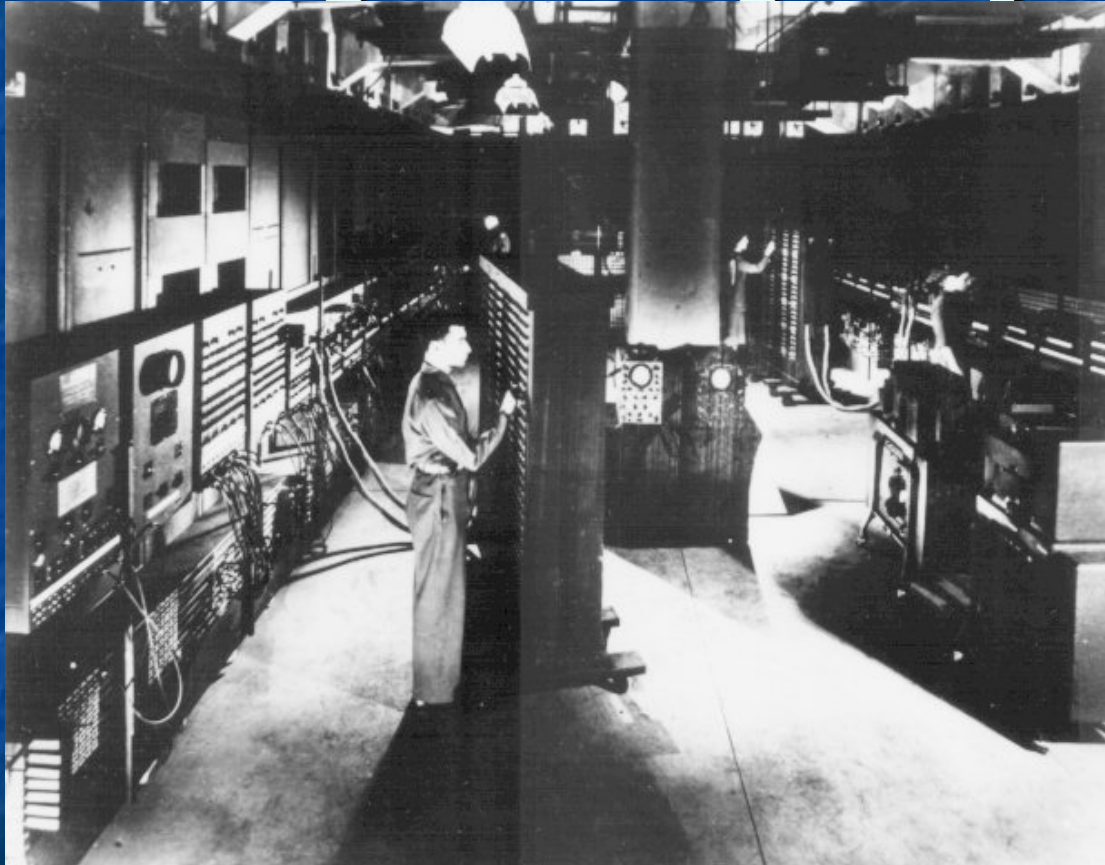
Software Failure Report

"The Fire Department [software community] seemed to be under the impression that they were required only to fight flames (bugs) and appeared surprised that their department was expected by the public to take every precaution (inspections, careful design, encapsulation, etc) to prevent fire (bugs) from starting."

Great Engineering Projects



Great Engineering Projects



Great Engineering Projects



Great Engineering Projects



Great Engineering Projects



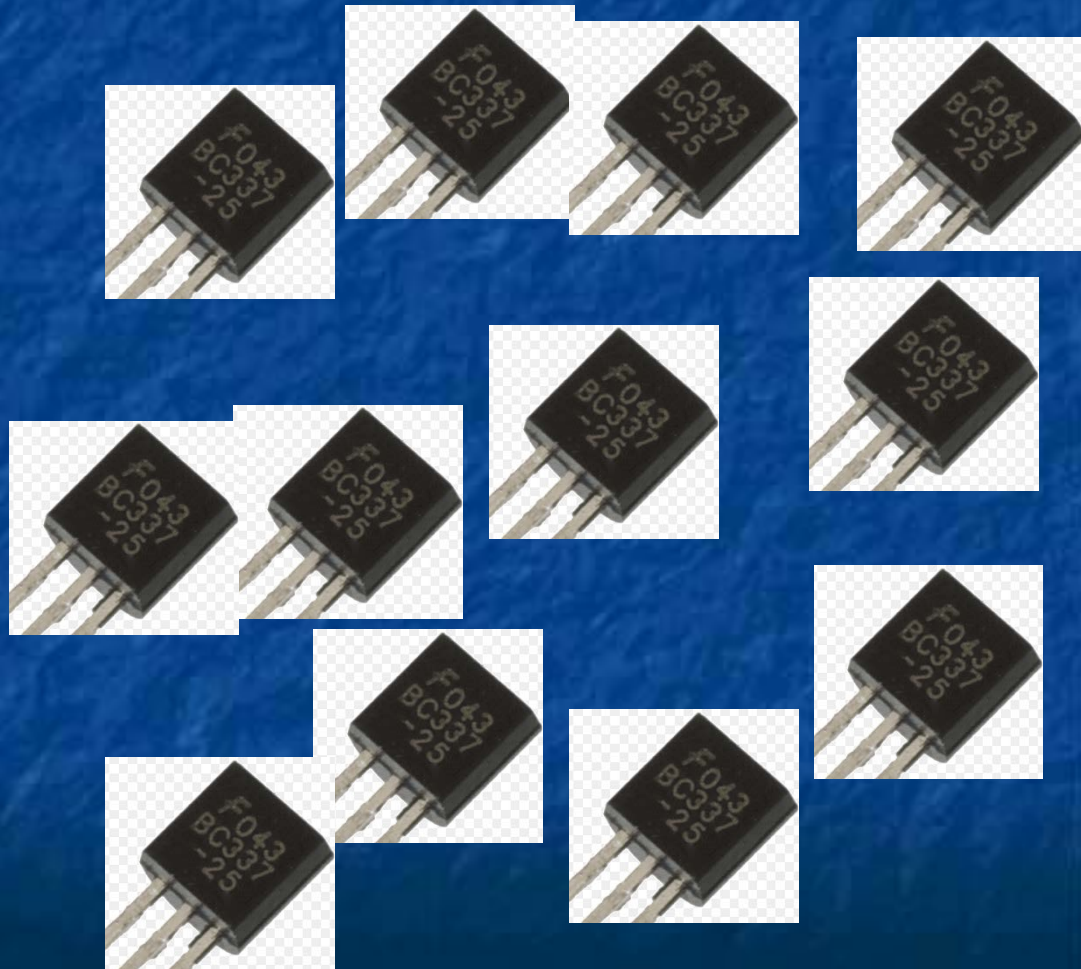
Great Engineering Projects



Great Engineering Projects



Or:



The Way Ahead

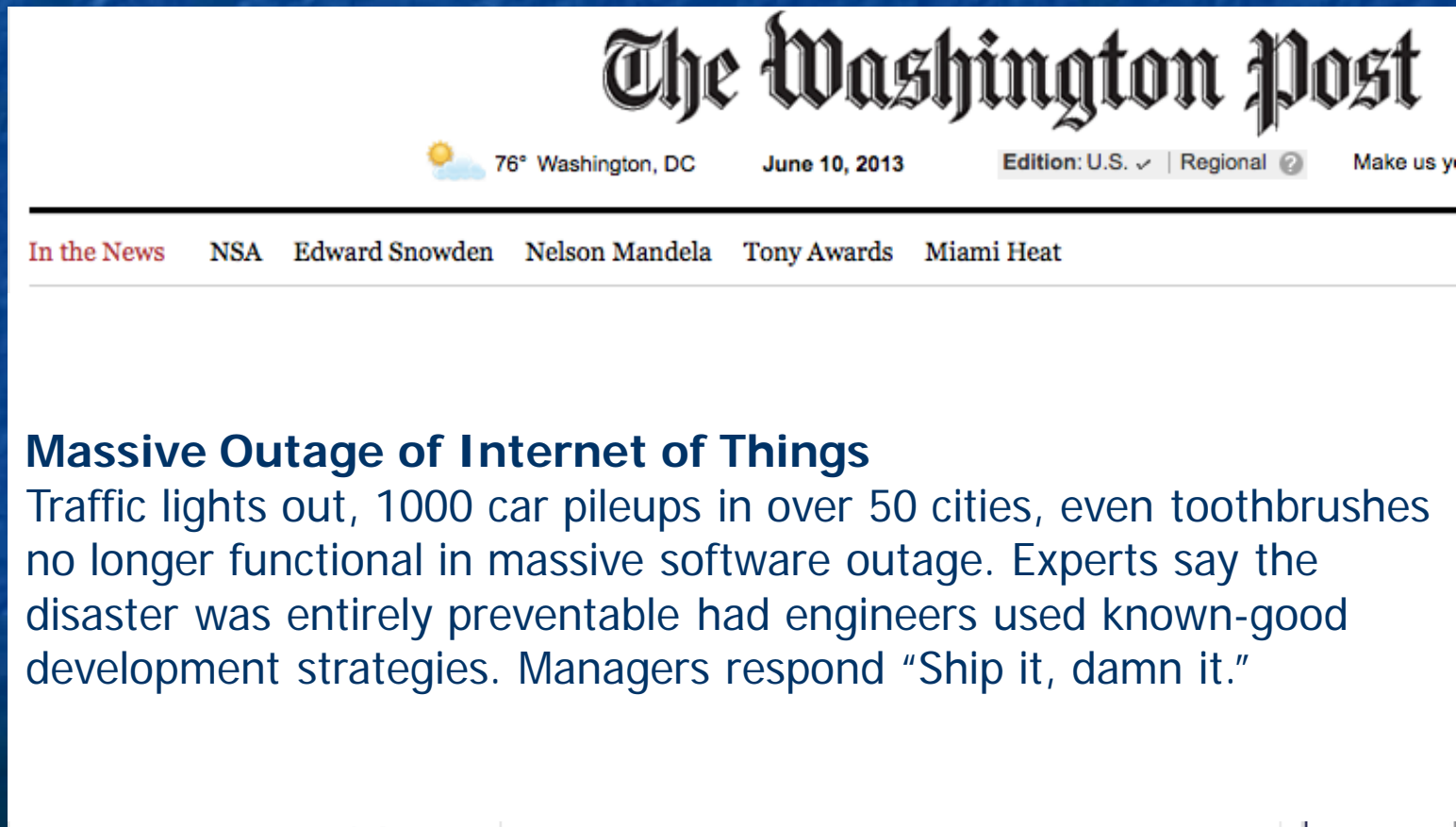


The Way Ahead



The Way Ahead

Litigation



The screenshot shows the top portion of The Washington Post's website. The masthead "The Washington Post" is in a large, black, serif font. Below it, a navigation bar includes a weather icon and "76° Washington, DC", the date "June 10, 2013", and edition options "Edition: U.S. ✓" and "Regional ?". A horizontal line separates this from a list of news topics: "In the News", "NSA", "Edward Snowden", "Nelson Mandela", "Tony Awards", and "Miami Heat". Below this line, the main article title "Massive Outage of Internet of Things" is displayed in a bold, dark blue font. The article text, also in dark blue, describes a widespread outage affecting traffic lights, cars, and even toothbrushes, attributing it to a preventable software failure and quoting managers as saying "Ship it, damn it."

The Washington Post

76° Washington, DC June 10, 2013 Edition: U.S. ✓ Regional ? Make us y

[In the News](#) [NSA](#) [Edward Snowden](#) [Nelson Mandela](#) [Tony Awards](#) [Miami Heat](#)

Massive Outage of Internet of Things

Traffic lights out, 1000 car pileups in over 50 cities, even toothbrushes no longer functional in massive software outage. Experts say the disaster was entirely preventable had engineers used known-good development strategies. Managers respond "Ship it, damn it."

The Way Ahead

Wall Street

Market Risk



QUANTITATIVE AND QUALITATIVE DISCLOSURES ABOUT MARKET RISK

RISKS

We are exposed to economic risk from foreign currency exchange rates, interest rates, credit risk, equity prices, and commodity prices. A portion of these risks is hedged, but they may impact our financial statements.

Foreign Currency

Certain forecasted transactions, assets, and liabilities are exposed to foreign currency risk. We monitor our foreign currency exposures daily and use hedges where practicable to offset the risks and maximize the economic effectiveness of our foreign currency positions. Principal currencies hedged include the euro, Japanese yen, British pound, and Canadian dollar.

The Way Ahead

Wall Street

Market Risk



QUANTITATIVE AND QUALITATIVE DISCLOSURES ABOUT MARKET RISK

RISKS

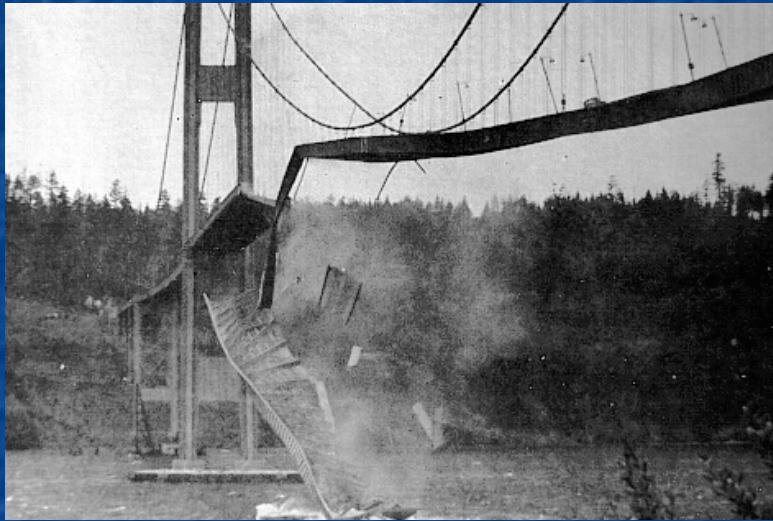
We are exposed to economic risk from foreign currency exchange rates, interest rates, credit risk, equity prices, and commodity prices. A portion of these risks is hedged, but they may impact our financial statements.

Software Engineering

We have elected to use development strategies known to lead to high bug rates, massive returns, and in some cases injury and/or death. These issues don't concern us at all, but it's reasonable to expect massive impacts to future financials.

The Way Ahead

Regulation (by catastrophe)



The Way Ahead

Education

- This is not software engineering:

```
long timer_read(void)
{
    unsigned int low, high;
    push_interrupt_state;
    disable_interrupts;
    low=inword(hardware_register);
    high=timer_hi;
    if(timer_overflow){++high;
        low=inword(hardware_register);}
    pop_interrupt_state;
    return (((ulong)high)<<16 + (ulong)low);
}
```


The Way Ahead

Strong management

- An absolute quality mindset.
- Complete intolerance of “artistes”
- Disciplined use of careful strategies that may not be considered “fun”.



ANY
QUESTIONS